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INFORMATION DISCLOSURE STATEMENT BY APPLICANT *(Use as many sheets as necessary)*		Application Number	Unassigned 10/567606
		Filing Date	Concurrently Herewith
		First Named Inventor	Volker SAUERMANN
		Art Unit	Unassigned
		Examiner Name	Unassigned
Sheet	1	of	1	Attorney Docket Number	09700.0200-00

U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS

Examiner Initials	Cite No.[1]	Document Number Number-Kind Code[2] *(if known)*	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-2002/0046316 A1	04-18-2002	Borowsky et al.	

Note: Submission of copies of U.S. Patents and published U.S. Patent Applications is not required.

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.[1]	Foreign Patent Document Country Code[3] Number[4] Kind Code[5] *(if known)*	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation[6]

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation[6]
		William Leinberger et al., "Multi-Capacity Bin Packing Algorithms with Applications to Job Scheduling under Multiple Constraints," retrieved from the Internet: https://www.users.cs.umn.edu/(karypis/publications/Papers/PDF/mrbinpack.pdf, May 27, 1999, pages 1-23	
		E. G. Coffman, Jr. et al., "Approximation Algorithms For Bin Packing: A Survey," Approximation Algorithms for NP-Hard Problems, retrieved from the Internet: http://www.ee.columbia.edu/(ego/webpapers/BPchapter.ps, 1996, pages 1-53.	
		"Relational Assignments for Distributed Database Systems," IBM Technical Disclosure Bulletin, IBM Corp., New York, Vol. 31, No. 1, June 1, 1988, pages 219-225.	
		Eric Jui-Lin Lu et al., "An Efficient Load Balancing Technique for Parallel FMA in Message Passing Environment," Proceedings of the Eighth SIAM Conference on Parallel Processing for Scientific Computing, retrieved from the Internet: http://www.cyut.edu.tw/(jlu/research/parallel/nbody/siam97.ps, March 1997, pages 1-8.	
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		PCT International Search Report, mailed November 5, 2004 (3 pages).	

Examiner Signature	/Douglas Bryant/	Date Considered	07/16/2009

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